

ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention	Method and Apparatus for Creating Circuit Redundancy in Programmable Logic Devices																																																																																																																										
<p>Submission Type : Utility Patent Filing</p> <p>Application Number: 10/708268 </p> <p>EFS ID: 55858</p> <table border="1" style="width: 100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th style="width: 30%;">Confirmation Code</th> <th style="width: 70%;">Message</th> </tr> </thead> <tbody> <tr> <td>ISVR1</td> <td>Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application</td> </tr> <tr> <td>ICON1</td> <td>2267</td> </tr> <tr> <td>WUTL2</td> <td>Filename= 1372_136_PRC-pkda.xml BusinessRule= The utility patent submission does not include at least one declaration.</td> </tr> </tbody> </table> <p>First Named Applicant: Praveen Samudrala</p> <p>Attorney Docket Number: 1372.136.PRC</p> <p>Timestamp: 2004-02-20 15:47:43 EDT</p> <p>From: us</p> <p>File Listing:</p> <table border="1" style="width: 100%; border-collapse: collapse; text-align: center;"> <thead> <tr> <th style="width: 30%;">Doc. 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